TEXAS A&M UNIVERSITY
SAN ANTONIO

Texas A&M University-San Antonio
College of Science and Arts
Computations, Engineering, Mathematics Sciences
Electronics Systems Engineering Technology
ESET

ESET 4202/4102, **Electronics Testing Lecture and Lab**
Fall 2026

Course Syllabus

Instructor:	**Maria F. Villa-Bracamonte**, Ph.D. E-Mail: mbracamonte@tamusa.edu Student emails will receive a reply within one business day.
Class Modality:	Face to Face (F2F)
Credit Hours:	**3** Credits (Lecture) +**1** Credit (Lab)
Meeting Time and Place:	**Lec.:** Monday 09:00 AM - 10:45 PM Room: STEM 243 **Lab:** Wednesday 08:00 AM – 10:45 PM, Room: STEM 245
Class Duration:	08/24 – 012\11
Course Website:	https://tamusa.blackboard.com/
Office Hours:	Email the professor for communication to zoom meeting (Mon – Fri)

Catalog Description: Overview of Testing - Defects, Failures, and Faults. Design Representation, VLSI Design Flow. Role of Simulation in Testing. Automatic Test Pattern Generation. Current Testing, Ad Hoc Test Techniques, Scan-Path Design, Boundary-Scan Testing, Built-in Self-Test, Memory Testing, Testing FPGAs and Microprocessors. Synthesis for Testability.

Course Description: This course combines the principles of electronic testing with the design, fabrication, and testing of Printed Circuit Boards (PCBs) through both theoretical and hands-on activities. Students will study defects, faults, and failures; engineering failure reporting; design representations and the VLSI design flow; fault modeling and simulation; automatic test pattern generation; and physical electronic testing and troubleshooting methods.

Students will apply these concepts throughout the PCB workflow, including schematic capture, component and footprint selection, PCB layout and routing, power distribution, high-speed and RF design considerations, design for manufacturing, fabrication, assembly, and testing using Altium Designer. The course also introduces current testing, ad hoc test techniques, scan-path and boundary-scan testing, built-in self-test, memory testing, FPGA and microprocessor testing, and design for testability.

The course emphasizes collaborative learning, real-world engineering practice, evidence-based fault detection and localization, and designing electronic systems that are easier to test and troubleshoot. Students will actively contribute to the course by preparing and delivering selected technical lectures, providing Altium Designer training to their classmates, and leading or guiding practical laboratory exercises. The course culminates in a final project involving the design, fabrication, and testing of a professional-grade PCB.

Prerequisite: ESET 3301, **Corequisite**: ESET 4102

Course Contents: (*Subject to change based on relevance and time)

- **PCB Design, Fabrication, and Testing**
  - PCB design workflow, including schematic capture, component selection, layout, routing, fabrication, and assembly.

    - Design-for-manufacturing considerations and PCB inspection.
    - PCB testing techniques, including continuity, power-up checks, signal integrity, functional testing, and thermal evaluation.
    - PCB troubleshooting, fault identification, and fault localization.
- **Test Hardware and Instrumentation**
    - Use of laboratory instruments such as digital multimeters, oscilloscopes, function generators, logic analyzers, and spectrum analyzers.
    - Measurement of voltage, current, resistance, frequency, timing, and waveforms.
    - Development and use of test points, custom test fixtures, and Automated Test Equipment (ATE).
    - **Test procedures, measurement documentation, and PASS/FAIL criteria.**
- **Fault Analysis, Simulation, and Test Generation**
    - Defects, faults, and failures in electronic circuits and systems.
    - Fault models, fault simulation, and fault coverage.
    - Fault activation, propagation, controllability, and observability.
    - Automatic Test Pattern Generation (ATPG) and test-vector development.
    - Current testing and other electronic fault-detection techniques.
- **Circuit and System Design for Testability**
    - Testing challenges in digital, analog, and mixed-signal electronic systems.
    - Design techniques that improve accessibility, controllability, observability, and fault localization.
    - Scan-path, boundary-scan/JTAG, and Built-In Self-Test (BIST) concepts.
    - Memory, FPGA, and microprocessor testing.
    - Integration of testability considerations during PCB and electronic-system design.
- **Practical Applications and Engineering Case Studies**
    - Real-world PCB design, fabrication, assembly, testing, and troubleshooting workflows.
    - Fault-injection and diagnostic exercises.
    - Analysis of electronic-system failures and engineering troubleshooting strategies.
    - Application of testing principles to improve reliability and maintainability.
    - Student-led technical presentations, Altium Designer training, and guided practical exercises.

Course Objectives:

- Provide students with a fundamental understanding of electronic testing principles, including defects, faults, failures, fault models, and test strategies.
- Introduce the complete PCB development and testing workflow, from schematic design and layout through fabrication, assembly, validation, and troubleshooting.
- Develop practical experience with electronic test instrumentation, test procedures, and custom test hardware or fixtures used to evaluate electronic circuits and systems.
- Introduce testing considerations for digital, analog, and mixed-signal circuits, including signal integrity and system-level integration.
- Introduce the use of simulation, fault simulation, and test-generation techniques for detecting and evaluating faults.
- Develop an understanding of Design for Testability (DFT) and techniques that improve controllability, observability, fault localization, and testing efficiency.
- Familiarize students with testing approaches including ATPG, current testing, scan-path testing, boundary-scan/JTAG, BIST, memory testing, and FPGA/microprocessor testing.
- Integrate theoretical testing concepts with hands-on PCB fabrication, measurement, troubleshooting, and hardware validation.

- Provide students with experience applying systematic testing workflows to evaluate, diagnose, and improve the functionality and reliability of electronic systems.

Learning Outcomes:

After taking this course, students are expected to have attained the following knowledge and abilities:

- Describe the steps involved in PCB design, fabrication, assembly, and testing, and explain the methods used to validate functionality and reliability.
- Identify and explain common defects, faults, and failures in electronic circuits and systems.
- Apply fault models, fault simulation, test vectors, and fault coverage concepts to evaluate electronic designs.
- Explain and apply fundamental concepts of Automatic Test Pattern Generation (ATPG) and other electronic fault-detection techniques.
- Operate test instruments such as multimeters, oscilloscopes, function generators, logic analyzers, and spectrum analyzers for electronic system evaluation.
- Develop and apply test procedures, test points, and custom test fixtures to support fault detection and troubleshooting.
- Evaluate circuit and PCB designs for testability and apply techniques that improve controllability, observability, and fault localization.
- Explain and apply concepts related to scan-path testing, boundary-scan/JTAG, Built-In Self-Test (BIST), memory testing, FPGA testing, and microprocessor testing.
- Design PCBs using Altium Designer, including schematic capture, component selection, layout, routing, and design-for-manufacturing considerations.
- Assemble, inspect, test, and troubleshoot PCBs in a laboratory environment using measurements and engineering evidence.
- Document and communicate test results, failures, troubleshooting procedures, and corrective actions using appropriate engineering practices.
- Prepare and deliver technical lectures, Altium Designer training, and guided practical exercises as part of collaborative course activities.
- Integrate design, fabrication, and testing concepts in a final project involving the development and validation of a functional PCB.

Required Materials: This course allows students to engage deeply with PCB design principles while developing hands-Additional and supplemental materials will be posted on Blackboard throughout the semester to support students' learning and enhance their experience with the course topics and activities. These materials may include lecture slides, selected readings, reference documents, useful webpages, and other resources that provide practical guidance and reinforce concepts covered in class.

Each student will prepare and deliver two-three lectures. This allows deeper exploration of the course material, additional practical exercises, and more comprehensive coverage of topics. Each student will lead lecture, incorporating a blend of theory, practical training, and project work.

Blackboard: Connect to http://tamusa.blackboard.com. You will have lecture notes, solutions to problems, multimedia materials, and other supplementary materials in Blackboard. All class communications will be through Blackboard, and students should monitor this several times a day.

Time Expectation for coursework: You are expected to spend 3-6 hours per week for the course. Based on the background, some students may require more time. Time spent may be longer when assignment/exams are due.

Course Grades and Polices:

- Final grades will be assigned using the weighted average of the following components.

- The instructor reserves the right to adjust the grading scale and/or weights based on the difficulty of work assigned throughout the semester.

- **Lecture Grading Policy**

  - **Lecture and Training Delivery (20%):**
    - Quality, clarity, and engagement in presentations.
    - Relevance and effectiveness of Altium training.
  - **Homeworks/ in-class assignments (20%):**
  - **Research paper (10%)**
  - **Quizzes (10%)**
  - **Final Project (40%):**
    - Innovation, accuracy, and completeness of PCB design.

- **Lab Grading Policy**

  - **Attendance 10%:** it is a must.
  - **Lab works 20 %:** Run the experiments
  - **Technical Reports 50%**
  - **Final project 30%:** hands-on Implementation

  Course grades are awarded as follows:
  A: Over 90.0%; B: 80.0 ~ 89.99%; C: 70 ~ 79.99%; D: 60.0 ~ 69.99%; F: less than 59.99%.

- **Quizzes**
  Short quizzes may be given periodically throughout the semester and will generally be administered at the beginning of class. Quizzes will assess concepts from recent lectures, programming activities, or assigned course materials. Students must be present when the quiz begins in order to take it. Students who arrive after the quiz has started will not be allowed to take the quiz. No late or make-up quizzes will be permitted.

- **Technical Report Submission Policy**
  Students must attend and participate in the class or laboratory session in which the corresponding activity is completed in order to submit the technical report. Students who miss the session are not eligible to submit the report and will receive no credit for that assignment. Technical reports are due on Sunday of the corresponding lab week.

  Lab Report: Due every Sunday. Template provided by the professor on Blackboard.

- **Final Project Evaluation**
  The Final Project will be evaluated separately in the lecture and laboratory components:
  - **Lecture:** Evaluation will focus on the **engineering and design aspects** of the project, including PCB design quality, design decisions, testing strategy, application of Design for Testability (DFT) concepts, technical documentation, and presentation.
  - **Laboratory:** Evaluation will focus on the **hands-on implementation and testing** of the project, including PCB fabrication and assembly, measurements, functionality, testing procedures, troubleshooting, validation, and overall hardware performance.

  Final Project: The project should demonstrate the student's ability to transfer the knowledge and skills acquired in the course to real-world applications. Formal Presentation, with PPT and report. Templates provided by professor.

TENTATIVE SCHEDULE - Electronics Testing Lec • Monday

Week	Dates	Electronics Testing Topic	PCB Topic
1	Aug. 24	Introduction, rubric review	
2	Aug. 31	Introduction to Electronic Testing	PCB Design Workflow and Basics
3	Sep. 7	Defects, Faults, Failures	Component Selection and Placement (1)
4	Sept. 14	Failure Reporting	Power Distribution and Decoupling (2)
5	Sept 21	Design Representation & VLSI Design Flow	PCB Design Rules and Constraints (3)
6	Sept. 28	Fault Models, Controllability, and Observability	PCB Design for Testability
7	Oct. 5	Simulation and Fault Simulation	Design for Manufacturing and Assembly (1)
8	Oct. 12	Automatic Test Pattern Generation	PCB Testing and Debugging (2)
9	Oct. 19	Practical Electronic Testing & Instrumentation	PCB Failure Modes and Troubleshooting (3)
10	Oct. 26	Current and Parametric Testing	
11	Nov. 2	Testing Memories, FPGAs and Microprocessors	
12	Nov. 9	Ad Hoc Techniques	
13	Nov. 16	Scan Paths and Boundary Scan	
14	Nov. 23	BIST and Synthesis for Testability	
15	Nov. 30	Final Review	
16	Dec. 7	Final Project Evaluation Week	

Student presentation assignments: (1), (2), and (3) indicate the assigned student presenter. PCB topics without a number are instructor-led. Students must email the instructor a brief outline of their proposed presentation, including the topics, examples, and demonstrations they plan to cover, no later than the Thursday before their scheduled presentation. This will allow time for feedback and any necessary adjustments before the presentation.

Note: This schedule is tentative and may be adjusted based on course progress and project needs.

Weekly Lecture Format

The Monday class will integrate the theoretical concepts of Electronic Testing with topics related to the PCB design project.

Instructor-Led PCB Topic Weeks

- Electronic Testing Topic — approximately 40 minutes
  - Instructor presentation of the scheduled Electronic Testing topic.
  - Key concepts, examples, and discussion.
- PCB Design Topic and Application — approximately 65 minutes
  - Instructor presentation of the scheduled PCB topic.
  - Connection of the topic to the semester PCB project.
  - Altium Designer demonstration, examples, guided activities, and/or discussion as appropriate.

Student-Led PCB Topic Weeks

- Electronic Testing Topic — approximately 35 minutes
  - Instructor presentation of the scheduled Electronic Testing topic.
- Student PCB Topic Presentation — approximately 35 minutes

- The assigned student presents the scheduled PCB topic based on independent research.
- The presentation should emphasize concepts relevant to PCB design, fabrication, testability, and the semester project.

- Altium Designer Demonstration and Application — approximately 35 minutes
  - Instructor-led demonstration of relevant Altium Designer tools and techniques.
  - Discussion of how the presented PCB concepts apply to the semester PCB project.

Note: Times are approximate and may be adjusted depending on the complexity of the lecture topic, student questions, demonstrations, and project progress

TENTATIVE SCHEDULE - Electronics Testing Lab • Wednesday

Lab	Date	Main activity	Deliverable / checkpoint
1	Aug. 26	Safety, DMM, power supply, breadboard measurements + PCB project overview/requirements	Instrument readiness + project understanding
2	Sept. 2	Altium intro: schematic + PCB workspace + create the real PCB project; begin schematic	Project file + initial schematic
3	Sept. 9	Oscilloscope/function generator basics + continue PCB schematic and identify important signals to measure	Waveform screenshots + schematic progress
4	Sept. 16	Component/circuit testing (RC, diode, transistor/sensor) + refine schematic and select major components	Short testing report + updated schematic
5	Sept. 23	Complete PCB schematic + verify inputs, outputs, power, connectors, and circuit blocks	Draft/final schematic
6	Sept. 30	Component selection, footprints, BOM + verify datasheets, packages, pin numbers, and component availability	BOM + footprint list
7	Oct. 7	PCB layout + begin complete board placement, routing, ground, and trace widths	Draft PCB layout
8	Oct. 14	Design for testability + add signal/ground test points + instructor design review	Fix list before ordering
9	**Oct. 21**	Final ERC/DRC, Gerbers, BOM, CPL + final corrections; submit for fabrication/assembly	Send for fabrication/assembly
10	Oct. 28	Oscilloscope fault-detection exercise + develop PCB test plan and expected measurements	Bring-up checklist
11	Nov. 4	Troubleshooting/fault-injection lab + prepare PCB debugging strategy and expected failure cases	Debugging practice
12	Nov. 11	PCB inspection and initial bring-up + visual inspection, continuity/power checks, first power-on	Power-on validation
13	Nov. 18	PCB measurements, troubleshooting, validation + compare measured signals with expected behavior	Measured results
—	Nov. 25	Study Day — no class	No lab meeting
14	Dec. 2	Final PCB validation/demonstration + complete functional testing; collect report data	Final demo + report
15	**Dec. 9**	Final Project Presentations + final PCB demonstration; contingency for remaining validation/troubleshooting	Final presentation + completed PCB project

Note: This schedule is tentative and may be adjusted based on project progress, fabrication/assembly turnaround, and course needs.

Class conducts and civility code: Everyone in class is expected to follow all rules in the student handbook, as well as common courtesy during classroom lectures and discussions in class and online, including the following:

1. Attendance may be taken at the beginning or the end of the class. A vital part of every student's education is regular attendance of class meetings.
2. It is the students' responsibility to obtain and be able to use the required materials and software

for this class.

3. Students should regularly check the Course Calendar, Announcements, and Messages (e-mail) systems in Blackboard.
4. Students should be prepared to turn in their assignments' solutions and must be uploaded to BB by their due date and time. **No late assignments will be accepted, nor are make-up allowed**.
5. If you cannot submit answers to any assignment online and are within the deadline. You can only get credit for your work by emailing the instructor your completed assignment. As possible, take pictures/snapshots of the Issue faced online so it can be addressed.
6. Students must retain copies of all assignments and graded work for verification purposes and provide it to the instructor, if necessary. Keep your own copies of all computer files and e-mails till the final grade is received.
7. Talking while the instructor is lecturing is extremely disruptive and discourteous to the instructor and other students.
8. Using computers or phones (except for a valid urgent need) during class for a purpose not related to class is disruptive. All cell phones and gadgets should be turned OFF and headphones removed.
9. For any questions about the exams and assignments, a student should contact the instructor well in advance of the day they are due, so the instructor may have enough time to provide feedback.
10. All communications will be via e-mail communications to the Texas A&M University e-mail account, and students are expected to use their school provided email account. The instructor will reply to student e-mail messages and voice messages within 2 business days (Monday-Friday).
11. For all classwork, exams, quizzes, etc., if a student is completing it off-campus, they are responsible for the availability of internet connectivity. Extensions will **not** be granted for lack of availability of internet connections.

The submission window may close or be marked late, even if late by one second. Anyone violating these policies may be subject to disciplinary actions.

Broader Use of Generative AI Permitted Within Guidelines: Artificial intelligence (AI) tools, including ChatGPT, are permitted in this course for students who wish to use them. To adhere to our scholarly values, students must cite any AI- generated material that informed their work (this includes in-text citations and/or use of quotations and in your reference list). Using an AI tool to generate content without proper attribution qualifies as academic dishonesty and violates Texas A&M-San Antonio's standards of academic integrity.

IMPORTANT POLICIES AND RESOURCES

Academic Accommodations for Persons with Disabilities: Texas A&M University-San Antonio is committed to providing all students with reasonable access to learning opportunities and accommodations in accordance with The Americans with Disabilities Act, as amended, and Section 504 of the Rehabilitation Act. If you experience barriers to your education due to a disability or think you may have a disability, please contact Disability Support Services in the Central Academic Building, Suite 210, or at (210) 784-1335 or visit https://www.tamusa.edu/index.html or email us at dss@tamusa.edu. Disabilities may include, but are not limited to, attentional, learning, mental health, sensory, physical, or chronic health conditions. All students are encouraged to discuss their disability-related needs with Disability Support Services and their instructors as soon as possible.

Academic Learning Center: The Academic Learning Center provides free course-based tutoring to all currently enrolled students at Texas A&M University-San Antonio. Students wishing to work with a tutor can make appointments through the Brainfuse online tutoring platform. Brainfuse can be accessed in the *Tools* section of Blackboard. You can contact the Academic Learning Center by emailing tutoring@tamusa.edu, calling (210) 784-1307, or visiting the Central Academic Building, room 202.

Counseling/Mental Health Resources: As a college student, there may be times when personal stressors interfere with your academic performance and/or negatively impact your daily functioning. If you are experiencing emotional difficulties or mental health concerns, support is available to you through the Student Counseling Center (SCC). To schedule an appointment, call 210-784-1331 or visit Madla 120. All mental health services provided by the SCC are free and confidential (as the law allows). The Student Counseling Center provides brief individual and group therapy, crisis intervention, consultation, case management, and prevention services.

Crisis support is available 24/7 by calling the SCC at 210-784-1331 (after-hours select option '2'). For more information and self-help resources, please visit www.tamusa.edu/studentcounseling.

Emergency Preparedness: JagE Alert is Texas A&M University-San Antonio's mass notification. In the event of an emergency, such as inclement weather, students, staff, and faculty, who are registered, will have the option to receive a text message, email with instructions and updates. To register or update your information visit: https://tamusa.bbcportal.com/. More information about Emergency Operations Plan and the Emergency Action Plan can be found here: https://www.tamusa.edu/about-us/campus-information/safety/university-police-department/documents/emergency-operations-plan.pdf and https://www.tamusa.edu/about-us/campus-information/safety/university-police-department/documents/emergency-action-plan.pdf Download the SafeZone App for emergencies or call (210) 784-1911. Non-Emergency (210) 784-1900

Financial Aid and Verification of Attendance: According to the following federal regulation, 34 CFR 668.21: U.S. Department of Education (DoE) Title IV regulation, a student can only receive Title IV funds based on Title IV eligibility criteria which include class attendance. If Title IV funds are disbursed to ineligible students (including students who fail to begin attendance), the institution must return these funds to the U.S. DoE within 30 days of becoming aware that the student will not or has not begun attendance. The faculty will provide the Office of Financial Aid with an electronic notification if a student has not attended the first week of class. Any student receiving federal financial aid who does not attend the first week of class will have their aid terminated and returned to the DoE. Please note that any student who stops attending at any time during the semester may also need to return a portion of their federal aid.

Writing, Language, and Digital Composing Center: The Writing, Language, and Digital Composing Center supports graduate and undergraduate students in all three colleges as well as faculty and staff. Tutors work with students to develop reading skills, prepare oral presentations, and

plan, draft, and revise their written assignments. Our language tutors support students enrolled in Spanish courses and students composing in Spanish for any assignment. Our digital studio tutors support students working on digital projects such as eportfolios, class presentations, or other digital multimedia projects. Students can schedule appointments through JagWire under the Student Services tab. Click on "Writing, Language, and Digital Composing Center" to make your appointment. The Center offers face-to-face, synchronous online, and asynchronous digital appointments. More information about what services we offer, how to make an appointment, and how to access your appointment can be found on our website at https://bit.ly/WLDCCenter.

Meeting Basic Needs: Any student who has difficulty affording groceries or accessing sufficient food to eat every day, or who lacks a safe and stable place to live and believes this may affect their performance in the course, is urged to contact the Dean of Students (DOS@tamusa.edu) for support. Furthermore, please notify the professor if you are comfortable in doing so. This will enable them to provide any resources they may possess.

Military Affairs: Veterans and active-duty military personnel are welcomed and encouraged to communicate, in advance if possible, and in special circumstances (e.g., upcoming deployment, drill requirements, disability accommodations). You are also encouraged to visit the Patriots' Casa in-person room 202, or to contact the Office of Military Affairs with any questions at military.va@tamusa.edu or (210)784-1397.

Religious Observances: Texas A&M University-San Antonio recognizes the diversity of faiths represented among the campus community and protects the rights of students, faculty, and staff to observe religious holidays according to their tradition. Under the policy, students are provided an opportunity to make up any examination, study, or work requirements that may be missed due to a religious observance provided they notify their instructors before the end of the second week of classes for regular session classes.

The Six-Drop Rule: Students are subject to the requirements of Senate Bill (SB) 1231 passed by the Texas Legislature in 2007. SB 1231 limits students to a maximum of six (6) non-punitive course drops (i.e., courses a student chooses to drop) during their undergraduate careers. A non-punitive drop does not affect the student's GPA. However, course drops that exceed the maximum allowed by SB 1231 will be treated as "F" grades and will impact the student's GPA.

Statement of Harassment and Discrimination: Texas A&M University-San Antonio is committed to the fundamental principles of academic freedom, equality of opportunity and human dignity. To fulfill its multiple missions as an institution of higher learning, A&M-San Antonio encourages a climate that values and nurtures collegiality and the uniqueness of the individual within our state, nation, and world. All decisions and actions involving students and employees should be based on applicable law and individual merit. Texas A&M University-San Antonio, in accordance with applicable federal and state law, prohibits discrimination, including harassment, based on race, color, sex, religion, national origin, age, disability, genetic information, veteran status, sexual orientation, gender identity, gender expression, or pregnancy/parenting status. Individuals who believe they have experienced harassment or discrimination prohibited by this statement are encouraged to contact the appropriate offices within their respective units.

Texas A&M University-San Antonio faculty are committed to providing a safe learning environment for all students and for the university. If you have experienced any form of sex- or gender-based discrimination or harassment, including sexual assault, sexual harassment, domestic or dating violence, or stalking, know that help and support are available. A&M-San Antonio's Title IX Coordinator can support those impacted by such conduct in navigating campus life, accessing health and counseling services, providing academic and housing accommodations, and more. The university strongly encourages all students to report any such incidents to the Title IX Coordinator. Please be aware that all A&M-San Antonio employees (other than those designated as confidential resources such as counselors and trained victim advocates) are required to report information about such

discrimination and harassment to the university. This means that if you tell a faculty member about a situation of sexual harassment or sexual violence, or other related misconduct, the faculty member must share that information with the university's Title IX Coordinator (titleix@tamusa.edu, 210-784-2061, CAB 439K). If you wish to speak to a confidential employee who does not have this reporting requirement, you can contact the Student Counseling Center at (210) 784-1331 or visit them in Madla 120.

Pregnant/Parenting Students: Texas A&M-San Antonio does not require a pregnant or parenting student, solely because of that status or issues related to that status, to (1) take a leave of absence or withdraw from their degree or certificate program; (2) limit the student's studies; (3) participate in an alternative program; (4) change the student's major, degree, or certificate program; or (5) refrain from joining or cease participating in any course, activity, or program at the University. The university will provide reasonable accommodations to pregnant students that would be provided to a student with a temporary medical condition and that is related to the health and safety of the student and the student's unborn child. These could include maintaining a safe distance from substances, areas, and activities known to be hazardous to pregnant individuals and their unborn child; excused absences because of illness or medical appointments; modified due dates for assignments; rescheduled tests/exams; taking a leave of absence; and being provided access to instructional materials and video recordings of lectures for excused absences, if these would be provided to any other student with an excused absence. Pregnant/parenting students are encouraged to contact the Title IX Coordinator with any questions or concerns related to their status (titleix@tamusa.edu; 210-784-2061; CAB 439K).

Texas A&M-San Antonio has also designated the Title IX Coordinator as the liaison officer for current or incoming students who are the parent or guardian of a child younger than 18 years of age. The Title IX Coordinator can provide students with information regarding support services and other resources.

Students' Rights and Responsibilities: The following statement of students' rights and responsibilities is intended to reflect the philosophical base upon which University Student Rules are built. This philosophy acknowledges the existence of both rights and responsibilities, which is inherent to an individual not only as a student at Texas A&M University-San Antonio but also as a citizen of this country.

Students' Rights

1. A student shall have the right to participate in a free exchange of ideas, and there shall be no university rule or procedure that in any way abridges the rights of freedom of speech, expression, petition, and peaceful assembly as set forth in the U.S. Constitution.
2. Each student shall have the right to participate in all areas and activities of the university, free from any form of discrimination, including harassment, based on race, color, national or ethnic origin, religion, sex, disability, age, sexual orientation, gender identity, gender expression, genetic information, or veteran status in accordance with applicable federal and state laws.
3. A student has the right to personal privacy except as otherwise provided by law, and this will be observed by students and University authorities alike.
4. Each student subject to disciplinary action arising from violations of university student rules shall be assured a fundamentally fair process.

Students' Responsibilities

1. A student has the responsibility to respect the rights and property of others, including other students, the faculty and staff, and the administration.
2. A student has the responsibility to be fully acquainted with and compliant with the University Student Rules found in the Student Handbook, Student Code of Conduct, on our website, and in the University Catalog.
3. A student has the responsibility to recognize that student actions reflect upon the individuals involved and upon the entire University community.

Students are expected to exhibit a high level of honesty and integrity in their pursuit of higher education. Students engaging in an act that violates the standards of academic integrity will find themselves facing academic and/or disciplinary sanctions. Academic misconduct is any act, or attempt, which gives an unfair advantage to the student. Additionally, any behavior specifically prohibited by a faculty member in the course syllabus or class discussion may be considered as academic misconduct. For more information on academic misconduct policies and procedures please review the Student Code of Conduct.

Important Dates:

August 24	First day of class
November 13	Last day to drop with an automatic "W"
November 30	Last day to withdraw from the University
Dec 04	Last day of classes
November 25	Study Day – No classes
Dec 5-11	Final exams

The complete academic calendar is available online: https://www.tamusa.edu/academics/academic-calendar/index.html